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Page <u>1</u> of <u>1</u>

PATENT NO. : 7,226,822 B2

APPLICATION NO.: 10/672,521

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INVENTOR(S) : Toru TAKAYAMA; Keiji SATO; Shunpei YAMAZAKI

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In The Claims:

Column 25, Claim 5, Line 31, Change "the wiring" to -- the gate electrode --.

Column 26, Claim 15, Line 23, Change "the wiring" to --the gate electrode--.

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